

PKG. CDDES

[S16+1]
[S16+1C]
[S16+2]
[S16+3]
[S16+3]
[S16+5]
[S16+5]
[S16+6]
[S16+6]
[S16+6]
[S16+7F]
[S16+8]
[S16+9F]
[S16+10]
[S16+10F]

## NOTES:

- 1. REFERENCE PKG, DUTLINE: 21-0041.
- 2. LAND PATTERN COMPLIES TO: IPC7351A.
- 3. TOLERANCE: +/- 0.001 in.
- 4. ALL DIMENSIONS APPLY TO BOTH LEADED (-) AND POFREE (+) PKG. CODES.

## -DRAWING NOT TO SCALE-

This document (including dimensions, notes & specs) is a recommendaton based on typical circuit board manufacturing parameters. Since land pattern design depend on many factors unknown to Maxim (eg. user's board manufacturing specs), user must determine suitability for use. This document is subject to change without notice.

Contact technical support at http://www.maxim-ic.com/support for further questions.

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PACKAGE LAND PATTERN, [S16] 0.150" SDIC, 16 LEADS

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