

PKG. CDDES

[A20+1]

[A20+2]

[A20+5]

[A20MS+6]

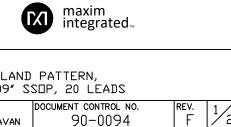
[A20MS+7]

[A20MS+7C]

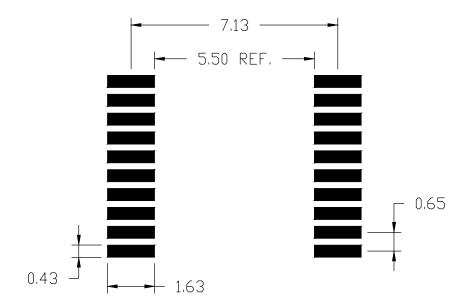
NDTES:

- 1. REFERENCE PKG. DUTLINE: 21-0056.
- 2. LAND PATTERN COMPLIES TO: IPC7351A.
- 3. TOLERANCE: +/- 0.02 MM.
- 4. ALL DIMENSIONS APPLY TO BOTH LEADED (-) AND POFREE (+) PKG. CODES.
- 5. ALL DIMENSIONS IN MM.

	-DRAWING NOT TO SCALE-	.	
	This document (including dimensions, notes & specs) is a recommendaton based on typical circuit board manufacturing parameters. Since land pattern design depend on many factors unknown to Maxim (eg. user's board manufacturing specs), user must determine suitability for use. This document is subject to change without notice. Contact technical support at http://www.maxim-ic.com/support for further questions.	ΠΤLE: PACKAGE LAND PATT [A20] 0.209" SSDP, 6	
		ROHINI KESAVAN	DOCUME



OPTION FOR IMPROVED HV ISOLATION (CLEARANCE/CREEPAGE OF 5.5MM)



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REV.

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TITLE:

PACKAGE LAND PATTERN, [A20] 0.209" SSDP, 20 LEADS

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