

PKG. CDDES

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NDTES:
1. REFERENCE PKG. QUTLINE: 21-0056.
2. LAND PATTERN CDMPLIES TD: IPC7351A
3. TILERANCE: +/- 0.02 MM.
4. ALL DIMENSIONS APPLY TV BDTH LEADED (-) AND PbFREE (+) PKG. CIDES.
5. ALL DIMENSIONS IN MM.
-DRAWING NOT TO SCALE-
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integrated

This document (including dimensions, notes \& specs) is a recommendaton based on typical circuit
board manufacturing parameters. Since land pattern design depend on many factors unknown to
Maxim (eg. user's board manufacturing specs), user must determine suitability for use
This document is subject to change without notice.
Contact technical support at http://www.maxim-ic.com/support for further questions.

TITLE:
PACKAGE LAND PATTERN, [A20] 0.209" SSDP, 20 LEADS


