



DS5002FP Secure Microprocessor Chip

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REVISION C3 ERRATA

The errata listed below describe situations where DS5002FP revision C3 components perform differently than expected or differently than described in the data sheet. Dallas Semiconductor intends to fix these errata in subsequent die revisions.

This errata sheet only applies to DS5002FP revision C3 components. Revision C3 components are branded on the topside of the package with a six-digit code of the form yywwC3, where yy and ww are two-digit numbers representing the year and work-week of manufacture, respectively. To obtain an errata sheet on another DS5002FP die revision, visit the website at www.maxim-ic.com.

1. STATE OF CARRY BIT CAN CAUSE HIGH STOP MODE CURRENT

Description:

Selection of Stop mode with the carry bit set will cause the I_{STOP} parameter to be as high as 450 μ A.

Work Around:

Clear the carry bit before selecting Stop mode.

2. V_{IH} VALUE FOR RST PIN EXCEEDS SPECIFICATION

Description:

The Input-High Voltage (V_{IH}) minimum specification for the RST pin will be 4.2V. The data sheet indicates that the value will be 3.5V.

Work Around:

None.