



# ERRATA SHEET

## DS21348/DS21Q348

### Revision A2 Errata

The errata listed below describe situations where DS21348/DS21Q348 revision A2 components perform differently than expected or differently than described in the data sheet. Maxim Integrated Products, Inc., intends to correct these errata in subsequent die revisions.

This errata sheet only applies to DS21348/DS21Q348 revision A2 components. Revision A2 components are branded on the topside of the package with a six-digit code in the form yywwA2, where yy and ww are two-digit numbers representing the year and work week of manufacture, respectively. To obtain an errata sheet on another DS21348/DS21Q348 die revision, visit our website at [www.maxim-ic.com/errata](http://www.maxim-ic.com/errata).

#### 1) MARGINAL 3V PULSE TEMPLATE

**Description:**

Output pulse shapes for the T1 and E1 line buildouts (LBOs) are marginal. DS21348 and DS21Q348 parts are tested using a modified pulse template. In E1 mode, DS21Q348 parts are tested with the alternative LBO settings provided in the table below.

**Workaround:**

Additional pulse height can be attained in E1 mode (ETS = 0) through one of the following LBO settings:

L2 (CCR4.7)	L1 (CCR4.6)	L0 (CCR4.5)	APPLICATION	TYPICAL PULSE HEIGHT INCREASE
0	1	0	75Ω normal	7% over LBO setting 0
0	1	1	120Ω normal	8% over LBO setting 1

#### 2) TRANSMIT OPEN-CIRCUIT DETECTION

**Description:**

The transmit open-circuit detect bit (TOCD) in the status register should be set when the device detects an open circuit at TTIP and TRING. The performance of this bit (SR.1) is unreliable, especially with sparse data patterns when B8ZS is not used, and it is not tested.

**Workaround:**

None.

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### 3) RECEIVE LEVEL INDICATION

**Description:**

The DS21348 and DS21Q348 should report the signal strength in RTIP and RRING in 2.5dB steps through the RL[3:0] bits in receive information register 2. The measurement of the receive level is accurate to  $\pm 2$  LSBs, i.e.,  $\pm 5$ dB.

**Workaround:**

None.

### 4) AUTOMATIC TRANSMIT UNFRAMED ALL ONES

**Description:**

When ATUA1 is enabled (CCR3.6 = 1), these devices do not automatically transmit an unframed all-ones pattern at TTIP and TRING during a carrier loss (RCL) condition.

**Workaround:**

None.