A similar version of this article appeared in the October 2012 issue of *Power Systems Design* magazine.

**Definition and Background**

In a radio frequency (RF) electrical transmission system, the standing wave ratio (SWR) is a measure of how efficiently RF power is transmitted from the power source, through the transmission line, and into the load. A common example is a power amplifier connected through a transmission line to an antenna.

SWR is, thus, the ratio between transmitted and reflected waves. A high SWR indicates poor transmission-line efficiency and reflected energy, which can damage the transmitter and decrease transmitter efficiency. Since SWR commonly refers to the voltage ratio, it is usually known as voltage standing wave ratio (VSWR).

**VSWR and System Efficiency**

In an ideal system 100% of the energy is transmitted from the power stages to the load. This requires an exact match between the source impedance, i.e., the characteristic impedance of the transmission line and all its connectors, and the load's impedance. The signal's AC voltage will be the same from end to end since it passes through without interference.

In real systems, however, mismatched impedances cause some of the power to be reflected back toward the source (like an echo). Reflections cause constructive and destructive interference, leading to peaks and valleys in the voltage at various times and distances along the line. VSWR measures these voltage variances. It is the ratio of the highest voltage anywhere along the transmission line to the lowest voltage.

Since the voltage does not vary in an ideal system, its VSWR is 1.0 or, as commonly expressed as a ratio of 1:1. When reflections occur, the voltages vary and VSWR is higher, for example 1.2, or 1.2:1.
Reflected Energy

When a transmitted wave hits a boundary such as the one between the lossless transmission line and load (Figure 1), some energy will be transmitted to the load and some will be reflected. The reflection coefficient relates the incoming and reflected waves as:

\[
\Gamma = \frac{V^-}{V^+} \tag{Eq. 1}
\]

Where \(V^-\) is the reflected wave and \(V^+\) is the incoming wave. VSWR is related to the magnitude of the voltage reflection coefficient (\(\Gamma\)) by:

\[
\text{VSWR} = \frac{(1 + |\Gamma|)(1 - |\Gamma|)}{(1 - |\Gamma|^2)} \tag{Eq. 2}
\]

![Figure 1. Transmission line circuit illustrating the impedance mismatch boundary between the transmission line and the load. Reflections occur at the boundary designated by \(\Gamma\). The incident wave is \(V^+\) and the reflective wave is \(V^-\).](image)

VSWR can be measured directly with an SWR meter. An RF test instrument such as a vector network analyzer (VNA) can be used to measure the reflection coefficients of the input port (\(S_{11}\)) and the output port (\(S_{22}\)). \(S_{11}\) and \(S_{22}\) are equivalent to \(\Gamma\) at the input and output port, respectively. The VNAs with math modes can also directly calculate and display the resulting VSWR value.

The return loss at the input and output ports can be calculated from the reflection coefficient, \(S_{11}\) or \(S_{22}\), as follows:

\[
\text{RL}_{\text{IN}} = 20\log_{10}|S_{11}| \text{ dB} \tag{Eq. 3}
\]
\[
\text{RL}_{\text{OUT}} = 20\log_{10}|S_{22}| \text{ dB} \tag{Eq. 4}
\]

The reflection coefficient is calculated from the characteristic impedance of the transmission line and the load impedance as follows:

\[
\Gamma = \frac{\text{Z}_L - \text{Z}_O}{\text{Z}_L + \text{Z}_O} \tag{Eq. 5}
\]

Where \(\text{Z}_L\) is the load impedance and \(\text{Z}_O\) is the characteristic impedance of the transmission line (Figure 1).

VSWR can also be expressed in terms of \(\text{Z}_L\) and \(\text{Z}_O\). Substituting Equation 5 into Equation 2, we obtain:

\[
\text{VSWR} = \left[1 + \left|\frac{\text{Z}_L - \text{Z}_O}{\text{Z}_L + \text{Z}_O}\right|\right]\left[1 - \left|\frac{\text{Z}_L - \text{Z}_O}{\text{Z}_L + \text{Z}_O}\right|\right] = \frac{\text{Z}_L + \text{Z}_O + |\text{Z}_L - \text{Z}_O|}{\text{Z}_L + \text{Z}_O - |\text{Z}_L - \text{Z}_O|}
\]

For \(\text{Z}_L > \text{Z}_O\), \(|\text{Z}_L - \text{Z}_O| = \text{Z}_L - \text{Z}_O\)

Therefore:
\[ \text{VSWR} = (Z_L + Z_O + Z_L - Z_O)/(Z_L + Z_O - Z_L + Z_L) = Z_L/Z_O. \]  \hspace{1cm} (Eq. 6)

For \( Z_L < Z_O \), \( |Z_L - Z_O| = Z_O - Z_L \)

Therefore:

\[ \text{VSWR} = (Z_L + Z_O + Z_O - Z_L)/(Z_L + Z_O - Z_O + Z_L) = Z_O/Z_L. \]  \hspace{1cm} (Eq. 7)

We noted above that VSWR is a specification given in ratio form relative to 1, as an example 1.5:1. There are two special cases of VSWR, \( \infty:1 \) and 1:1. A ratio of infinity to one occurs when the load is an open circuit. A ratio of 1:1 occurs when the load is perfectly matched to the transmission-line characteristic impedance.

VSWR is defined from the standing wave that arises on the transmission line itself by:

\[ \text{VSWR} = |V_{\text{MAX}}|/|V_{\text{MIN}}| \]  \hspace{1cm} (Eq. 8)

Where \( V_{\text{MAX}} \) is the maximum amplitude and \( V_{\text{MIN}} \) is the minimum amplitude of the standing wave. With two super-imposed waves, the maximum occurs with constructive interference between the incoming and reflected waves. Thus:

\[ V_{\text{MAX}} = V^+ + V^- \]  \hspace{1cm} (Eq. 9)

for maximum constructive interference. The minimum amplitude occurs with deconstructive interference, or:

\[ V_{\text{MIN}} = V^+ - V^- \]  \hspace{1cm} (Eq. 10)

Substituting Equations 9 and 10 into Equation 8 yields

\[ \text{VSWR} = |V_{\text{MAX}}|/|V_{\text{MIN}}| = (V^+ + V^-)/(V^+ - V^-) \]  \hspace{1cm} (Eq. 11)

Substitute Equation 1 into Equation 11, we obtain:

\[ \text{VSWR} = V^+/(1 + |\Gamma|)/(V^+/(1 - |\Gamma|)) = (1 + |\Gamma|)/(1 - |\Gamma|) \]  \hspace{1cm} (Eq. 12)

Equation 12 is Equation 2 stated at the beginning of this article.

**VSWR Monitoring System**

The **MAX2016** is a dual logarithmic detector/controller used to monitor the VSWR/return loss of an antenna, when it is paired with a circulator and attenuator. The MAX2016 outputs the difference between the two power detectors.

The MAX2016 combined with the **MAX5402** digital potentiometer and **MAX1116/MAX1117** ADC forms a complete VSWR monitoring system (Figure 2). The digital potentiometer acts as a voltage-divider by using the MAX2016’s reference voltage output. The internal reference voltage can typically source 2mA of current. This voltage sets the threshold voltage for the internal comparator (pin CSETL). An alarm can be generated when the output voltage crosses the threshold (pin COUTL). The MAX1116 ADC requires a 2.7V to 3.6V supply, while the MAX1117 ADC requires 4.5V to 5.5V. The ADC can also use an external reference voltage, provided by the MAX2016. The ADC paired with the microcontroller allows for constant monitoring of the antenna’s VSWR.
Figure 2. The VSWR monitoring system consists of an ADC for real-time measurements. The external digital potentiometer enables a configurable alarm signal at the comparator's output (COUTL).

Summary

In review, this tutorial describes SWR or VSWR as a way to measure transmission line imperfections and efficiency. VSWR is related to the reflection coefficient. A higher ratio depicts a larger mismatch, while 1:1 ratio is perfectly matched. This match or mismatch arises from the standing wave's maximum and minimum amplitude. SWR is related to the ratio between transmitted and reflected energy. The MAX2016 is shown as an example of how to create a system to monitor antenna VSWR.

<table>
<thead>
<tr>
<th>Related Parts</th>
<th>Description</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAX1116</td>
<td>Single-Supply, Low-Power, Serial 8-Bit ADCs</td>
<td>Free Samples</td>
</tr>
<tr>
<td>MAX1117</td>
<td>Single-Supply, Low-Power, 2-Channel, Serial 8-Bit ADCs</td>
<td>Free Samples</td>
</tr>
<tr>
<td>MAX2016</td>
<td>LF-to-2.5GHz Dual Logarithmic Detector/Controller for Power, Gain, and VSWR Measurements</td>
<td>Free Samples</td>
</tr>
<tr>
<td>MAX5402</td>
<td>256-Tap, μPoT Low-Drift, Digital Potentiometer</td>
<td>Free Samples</td>
</tr>
</tbody>
</table>

More Information

For Technical Support: [http://www.maximintegrated.com/support](http://www.maximintegrated.com/support)
For Samples: [http://www.maximintegrated.com/samples](http://www.maximintegrated.com/samples)
Other Questions and Comments: [http://www.maximintegrated.com/contact](http://www.maximintegrated.com/contact)

Application Note 5432: [http://www.maximintegrated.com/an5432](http://www.maximintegrated.com/an5432)
TUTORIAL 5432, AN5432, AN 5432, APP5432, Appnote5432, Appnote 5432
© 2012 Maxim Integrated Products, Inc.