



Keywords: single pole RC filter, output voltage, step response, step input voltage, accuracy final value

APPLICATION NOTE 5874

SETTLING TIME CALCULATOR TUTORIAL

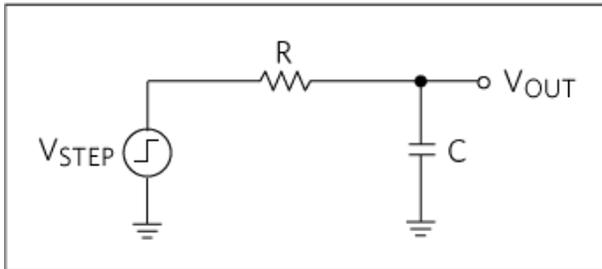
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Abstract: The Settling Time Calculator (STC) aids in the analysis and design of the step response of a single pole RC filter. The program is for use with an HP® 50g calculator or free PC emulator.

Introduction

Steve Edwards*, an experienced analog design engineer, has written several calculators to automate repetitive tasks. These tools are being shared to help other analog design engineers who select, specify, and characterize analog circuits. We will summarize the functionality of one such tool, the Settling Time Calculator.

The Settling Time Calculator (STC) is a program written for the HP50g calculator that aids in the analysis and design of the step response of a single pole RC filter. STC finds the time it takes (t_s) for the output voltage to respond to a step input voltage (V_{STEP}) and settle to within a specified fraction (Accu) of the final value. Three additional circuit parameters are found: time constant, cutoff frequency, and rise time.



Ten parameters can be entered or found,

- Resistance, R, in $k\frac{1}{2}$
- Capacitance, C, in nF
- Settling Time, t_s , in μs
- Accuracy, Accu, in τ
- Accuracy, Accu, in PPM
- Accuracy, Accu, in %
- Accuracy, Accu, in μV
- Accuracy, Accu, in LSB
- Voltage Step Size, V_{step} , in V
- Resolution, Res, in bits

STC can find any of these parameters as a function of the others, making it useful for both design and analysis.

Three additional circuit parameters are found,

- Time Constant, τ , in μs
- Cutoff Frequency, F_c , in kHz
- Rise Time, t_r , in μs

```

R = 1.0000 kΩ
C = 1.0000 nF
ts = 11.5129 μs
Accu = 11.5129 μs
Accu = 10.0000 PPM
Accu = 100.0000 μV
Vstep = 10.0000 V
τ = 1.0000 μs
NAME STO RCL PLOT FIND EXIT

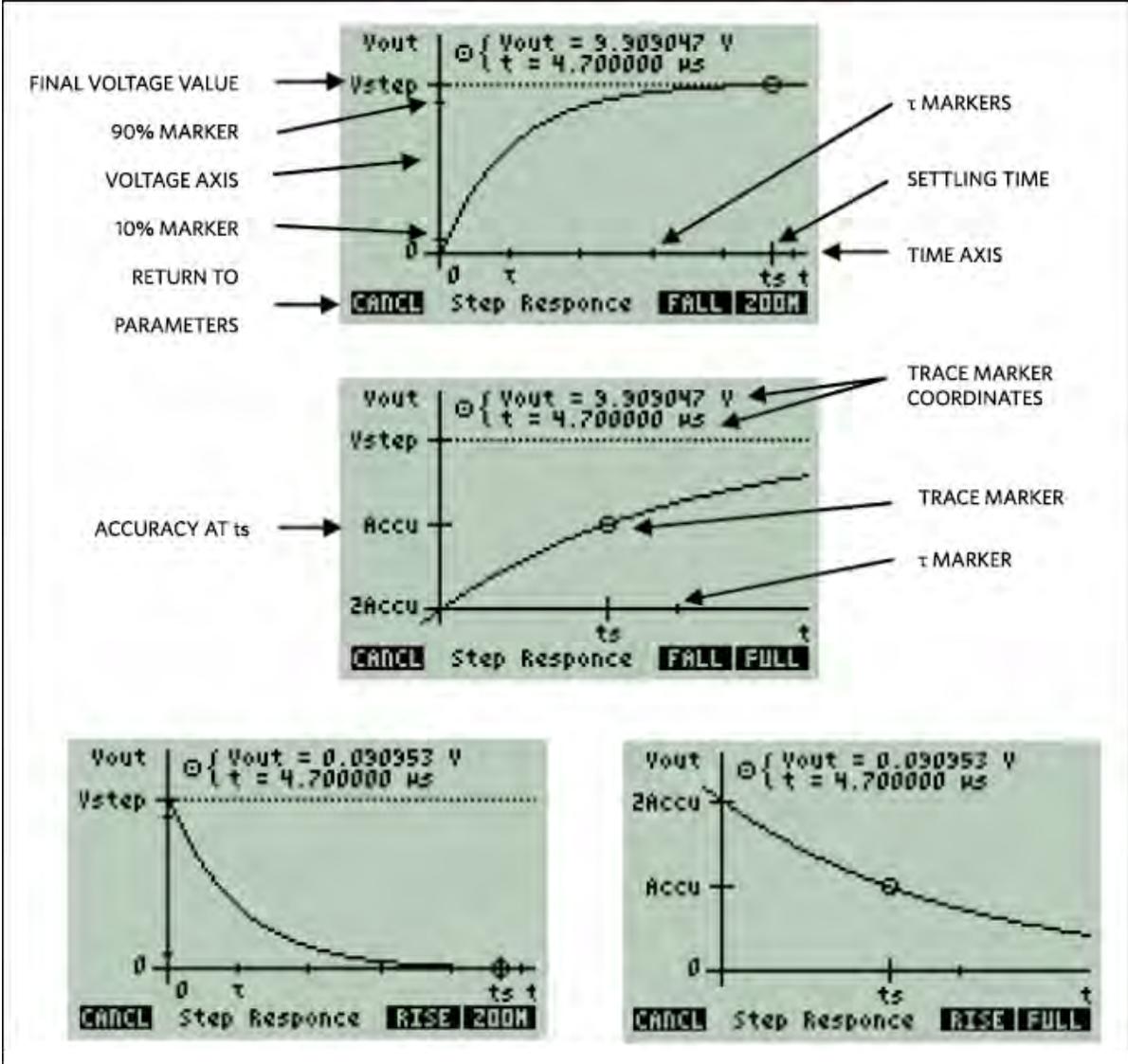
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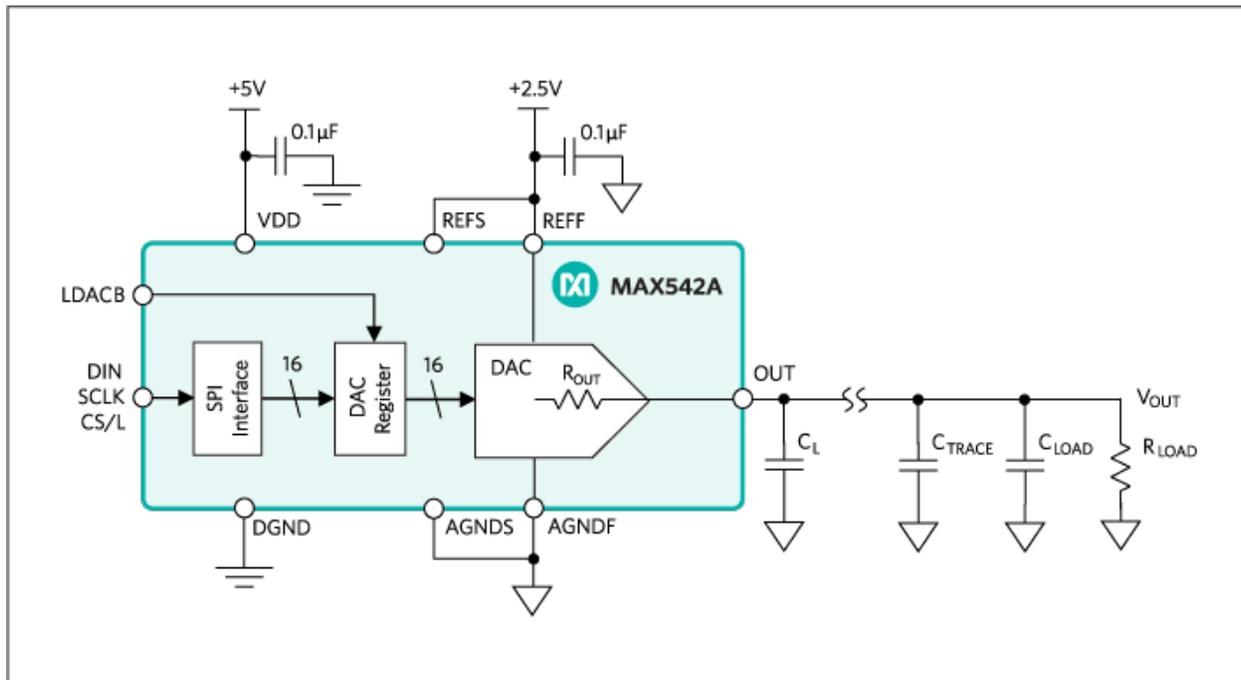
R = 1.0000 kΩ
C = 1.0000 nF
ts = 11.5129 μs
Accu = 11.5129 μs
Accu = 0.0010 %
Accu = 0.0102 LSB
Res = 10.0000 bit
Fc = 1591.5494 kHz
NAME STO RCL PLOT FIND EXIT

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The key elements of the plot display are shown below:



The calculator user's guide details an example, STC is used to predict the settling time of the output voltage of a precision digital to analog convertor (DAC) under different load conditions. Trade-offs between settling time, accuracy, and load are examined. The [MAX542A](#) precision DAC is used as an example. The application circuit is shown below.



*Steve Edwards is no longer with Maxim.

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Related Parts

[MAX542](#)

+5V, Serial-Input, Voltage-Output 16-Bit DACs

[Free Samples](#)

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