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APPLICATION NOTE 4281

Advanced Power-Line Monitoring Requires a High-Performance, Simultaneous-Sampling ADC

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Abstract: This application note explains how a simultaneous-sampling ADC like the MAX11046 can be used for advanced power-line monitoring. The article describes the fundamentals of AC power measurement by sampling. It shows a typical power-grid monitoring application and explains how international standards affect monitoring system requirements. The article summarizes the unique benefits of the MAX11046 for power-line monitoring and provides an application example.

Introduction

Power quality systems monitor in real time and record the voltages and currents from each of three phases of the multiphase power feed from power utilities. These supervisory control and data acquisition (SCADA) systems may also monitor the voltages and currents on the neutral line, thus sensing imbalanced loads or frequency harmonics. Additionally, power-metering systems measure the root mean squared (RMS) voltage and current from each of the three phases to determine power consumption.

Advanced power-line monitoring systems combine power quality, monitoring protection, and metering functions in one system. These systems allow the power utilities and customers to perform predictive maintenance, manage energy consumption and cost, control quality, and protect their equipment—all now performed more efficiently.

Measurement System Architecture

Figure 1 demonstrates the fundamentals of the AC power measurement by sampling calculations for instantaneous and average power usage. **Figure 2** shows a typical power-grid monitoring application. The 3-phase power design shown in Figure 2 refers to three voltages that are offset in phase from each other by 120° , i.e., by one third of a cycle. A fourth wire called neutral is used to accommodate imbalanced loads. If loads on each of the three phases are equal, the system is balanced and no current flows through the neutral line.

This type of the 3-phase power system is standard throughout the world. It is called a wye connection because of the way it appears on a phasor diagram. Each power phase's measurements are represented by a current transformer (CT) and a voltage transformer (PT). The complete system comprises four such pairs (one pair for each of the three phases plus neutral).

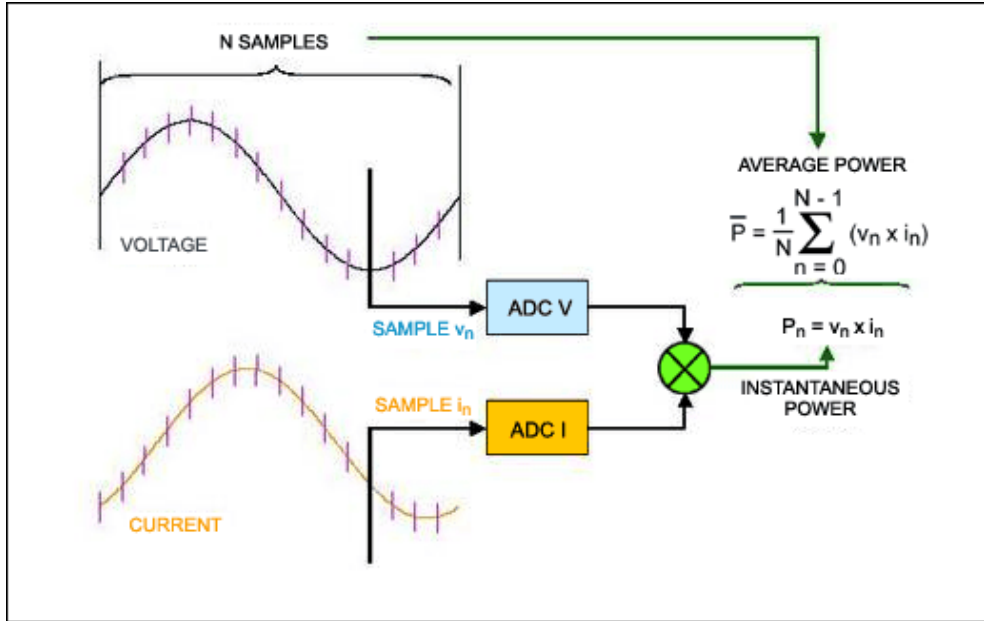


Figure 1. Illustration of AC power measurement by sampling.

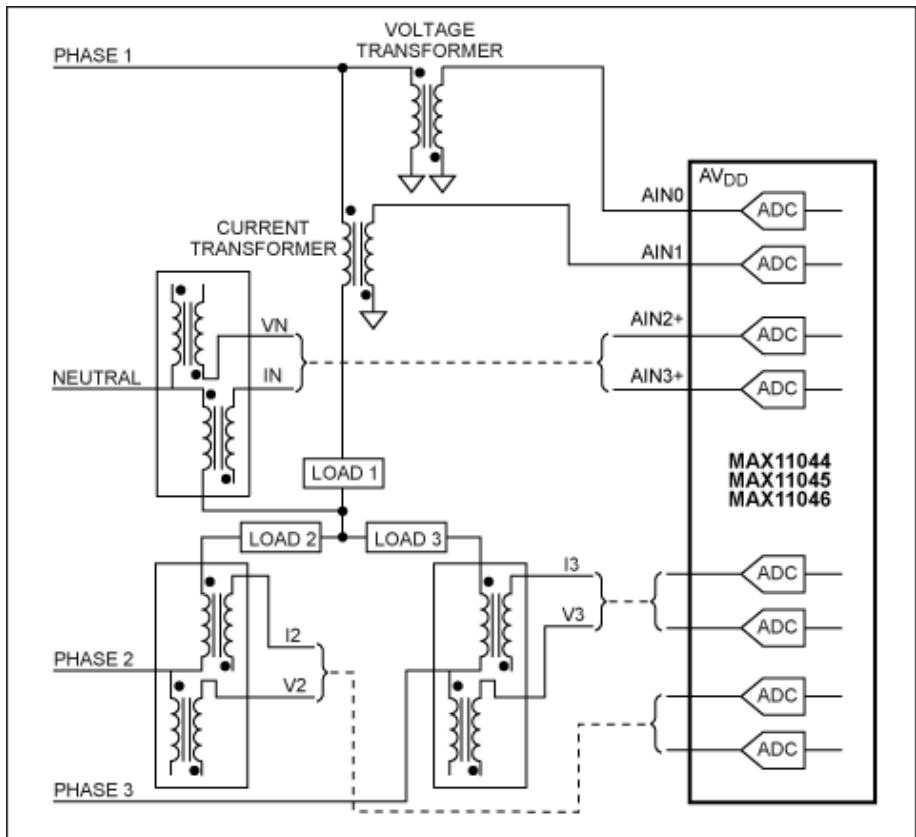


Figure 2. Typical power-grid monitoring application. The wye design is standard throughout the world.

As Figure 2 illustrates, the [MAX11044/MAX11045/MAX11046](#) simultaneously measure the three phases and a neutral voltage plus the currents. By performing digital processing calculations on the sampled and digitally converted data, the active, reactive, apparent energy and power factor parameters can be found. Frequency

and harmonic distortion metering can be achieved by performing a Fast Fourier Transform (FFT) on the sampled data.

The measurement characteristics of the delivered energy must comply with international requirements or local standards. An example of international requirements is the European Union (EU) standard EN 50160. **Table 1** summarizes the EN 50160 requirements:

Table 1. EN 50160 Energy Specifications

Supply Voltage Phenomenon	Acceptable Limits	Measurement Interval	Monitoring Period (week)	Acceptance Percentage (%)
Grid Frequency	49.5Hz to 50.5Hz, 47Hz to 52Hz	10s	1	95, 100
Slow Voltage Changes	230V \pm 10%	10min	1	95
Voltage Sags or Dips (\leq 1min)	10 to 1000 times per year (under 85% of nominal)	10ms	1 year	100
Short Interruptions (\leq 3min)	10 to 100 times per year (under 1% of nominal)	10ms	1 year	100
Accidental, long interruptions ($>$ 3min)	10 to 50 times per year (under 1% of nominal)	10ms	1 year	100
Temporary Overvoltages (line to ground)	Mostly $<$ 1.5kV	10ms	–	100
Transient Overvoltages (line to ground)	Mostly $<$ 6kV	–	–	100
Voltage Unbalance	Mostly 2%, but occasionally 3%	10min	1	95
Harmonic Voltages	8% total harmonic distortion (THD)	10min	1	95

Another EU standard, IEC 62053, mandates the precision of energy metering equipment. It defines four classes of meters: Class 2, Class 1, Class 0.5, and Class 0.2. (As an example, Class 0.2 requires meter precision to be 0.2% of nominal current and voltage). For power-factor accuracy measurement, phase matching should be 0.1% or better.

For the harmonic voltage, EN 50160 mandates measurement up to the 25th order harmonic of 50Hz/60Hz voltages. However, propagation of the various "nonlinear" loads such as fluorescent lamps and switching power supplies drives measurement up to the 127th order harmonic of 50Hz/60Hz voltages.

Emerging standards like IEC 61850 recommend the recording of power-system transient events with 256 samples per AC cycle or higher.

Basic ADC System Requirements

Standards like EN 50160, IEC 62053, and IEC 61850 dictate both the minimum accuracy and sample rate needed for a modern multichannel ADC system used in power-system monitoring and metering.

Power-monitoring equipment must measure instantaneous current and voltage values with sample rates up to 60Hz x 256 samples, or greater than 15360sps. Moreover, the power monitoring should be capable of measuring power accurately enough to accommodate the IEC 62053 standard requirements.

The ADC's dynamic range for voltage measurement can be calculated from the maximum and nominal voltages to be monitored and from the required accuracy for power measurements (see Table 1 and

comments on the IEC 62053). For example, if a design must measure a 1.5kV (1500V) temporary overvoltage (under a fault condition) with a nominal 220V voltage measurement and a Class 0.2 (0.2%) accuracy requirement, then the total dynamic range of the voltage-measurement subsystem will need to be:

$$20\log ((1500/220) \times 2000) = 83\text{dB}$$

Note: In all the calculations we assumed an intended accuracy of 0.05%, which is substantially below the standard 0.2% accuracy requirements.

Additionally, current-sensing requirements also effect ADC specifications. If the design requirements for power monitoring are the typically 100A:10A (10A nominal and 100A maximum) and Class 0.2 (0.2 %), then the total dynamic range of the current measurement subsystem will need to be:

$$20\log ((100/10) \times 2000) = 86\text{dB}$$

The examples above clearly demonstrate the increasing need for higher performance in ADCs. Today 16-bit resolution at sampling rates of 16ksps, or higher, is essential. To ensure accurate 3-phase and neutral wye system current and voltage measurements, the ADC must be capable of sampling eight channels simultaneously (4 voltage and 4 current), and it must have an excellent SNR of 86dB or better.

Power-Line Monitoring Capabilities of the MAX11046 ADC

The MAX11046 includes eight simultaneous-sampling, low-power, 16-bit, 250ksps, successive-approximation ADCs in a single package. It is ideally suited to power-system monitoring and measurement applications:

- $\pm 5\text{V}$ input capability with only a +5V supply
- The high-input impedance values of the MAX11046 family, as well as self-protecting input clamps allow direct connection to low-output impedance sensors including CT and PT measurement transformers; no external buffer is required.
- Synchronized sampling allows extremely accurate angle measurements between voltage and currents on each line phase.

Figure 3 summarizes the unique benefits of the MAX11046.

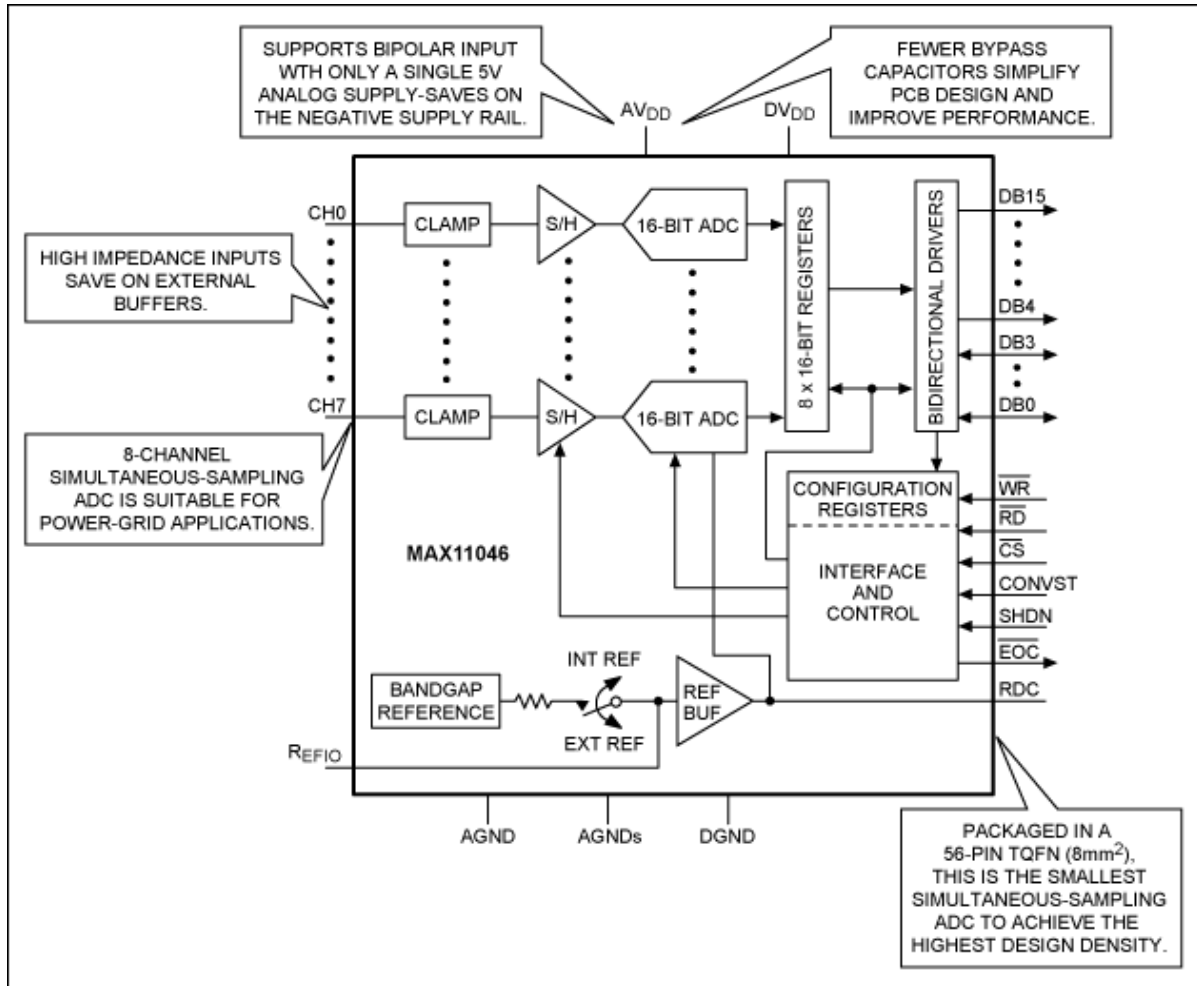
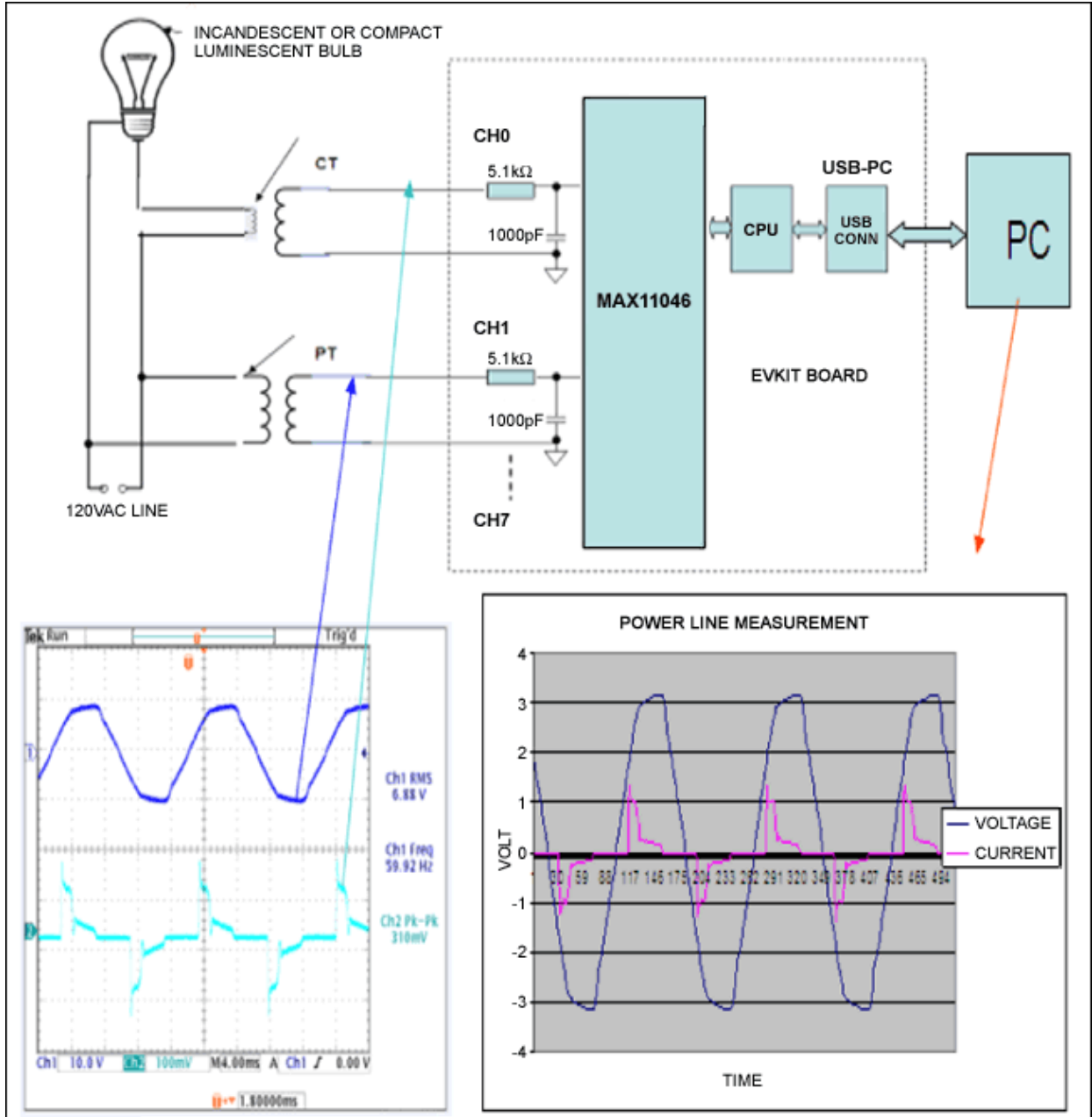


Figure 3. Description of the unique capabilities of the MAX11046 for power-measurement monitoring.

Figure 4 shows an application example generated with the MAX11046 evaluation (EV) kit board connected to power-line monitor transformers. The schematic shows the simple cost and space-efficient interface between the power-line transformers and the MAX11046.

The left oscilloscope plot shows the current and voltages from a compact fluorescent bulb, taken from the CT (current) and PT (voltage) transformers. This configuration was used as a load and test example. The software plot at the right shows the results of the CT and PT signals acquisition and reconstruction post digitization.



More detailed image (PDF, 759KB)
 Figure 4.

Conclusion

Rising worldwide power demands are driving increased investment in the power-delivery infrastructure. A critical element of these new power delivery systems are multichannel supervisory control and data acquisition (SCADA) systems, which include digital relays for automated power-delivery monitoring, and fault-detection and protection. SCADA systems require multichannel high-resolution ADCs such as MAX11046.

The MAX11046 offers the most efficient 16-bit, 8-channel simultaneous sampling in an 8mm x 8mm footprint. It eliminates external buffers due to its high-impedance input structures. The device is optimized for 3-phase power-monitoring and measurement systems, and a natural choice for high-density designs that need to

reduce cost and board area while increasing performance.

Related Parts

[MAX11046](#)

4-/6-/8-Channel, 16-/14-Bit, Simultaneous-Sampling ADCs

[Free Samples](#)

More Information

For Technical Support: <http://www.maximintegrated.com/support>

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