APPLICATION NOTE 3736

How to Test the Crystal's Power Level in the MAX9485

Mar 20, 2006

Abstract: MAX9485 is a versatile, programmable, audio clock generator. It consists of a VCXO, fractional-N PLL, and I²C interface. To use the MAX9485 appropriately, it is important that the power level of the VCXO's crystal does not exceed its maximum rating. This application note describes a simple method for measuring the power level of a crystal oscillator, and reports the test results on the MAX9485.

Introduction

The MAX9485[1, 2] is a cost-effective, programmable, multiple-output audio clock generator for MPEG-2 or AC-3 audio systems such as DVD players, a DVD drive for multimedia PC's, HDTV's, home entertainment centers, and set-top boxes. The MAX9485 has a fractional-N programmable PLL with two output drivers, a VCXO with ±200 PPM pulling range, and an I²C interface. Its system functional diagram is shown in Figure 1.
When using the MAX9485, the crystal’s power level should be less than the maximum rating given by the crystal’s manufacturer. If the crystal’s driving level is higher than its maximum rating, the crystal’s equivalent serial resistance (ESR) and frequency accuracy may degrade. If the crystal’s power were 10x over its maximum rating, it could cause irreversible damage to the crystal.

In this application note, we first review the formula for calculating a crystal’s power level. We then introduce a simple test setup for measuring the crystal’s power level, and finally present the test result from the MAX9485.

**Calculation of a Crystal's Power Level**

Figure 2 illustrates a circuit that functions equivalent to a crystal[3].

![Figure 2. Circuit diagram of a crystal Equivalent.](image)

The ESR R, shown in Figure 2, of a crystal causes the crystal to consume power. If the RMS (root of mean square) current going through R is $I_R$, then we can obtain the crystal’s power level by;

$$P_c = I_R^2 \times R \quad \text{(Eq. 1)}$$
However, in practice, $I_R$ is not measurable. When a crystal is resonating, the impedance of the LC branch is much smaller than that of the C0 branch. Therefore, we can use $I_C$ to approximate $I_R$. Thus, we get a practical formula for the crystal's power-level calculation:

$$P_c = I_C^2 \times R \quad \text{(Eq. 2)}$$

The current going through the crystal is AC and its waveform is often sinusoidal or saw-tooth. The following relations determine the RMS values of sine-wave and saw-tooth wave currents from their peak-to-peak amplitudes.

Sine Wave: \[ I_C = \frac{1}{2\sqrt{2}} I_{PP} \quad \text{(Eq. 3)} \]

where $I_{PP}$ is the peak-to-peak value of the current.

**Crystal Power-Level Test Setup**

For the MAX9485, we recommend that you use the 27MHz crystal (DSX530GA)[4] from KDS America. This crystal has a rated maximum power level of 300µW.

Measuring the crystal's current is not straightforward. In the test setup shown in Figure 3, we use a small 10Ω resistor to sense the current. We know that using the 10Ω resistor is valid, because changing the resistor to 20Ω barely changed the sensed current.

**Figure 3. Setup for testing a crystal's power level.**

**Measurement Data and Power-Level Calculation**

Using the test setup in Figure 3, the highest crystal power level occurs at 27MHz, which is around the center of the VCXO tuning range. The following plots (Figures 4, 5, and 6) are the measured voltage waveforms across the current-sensing resistor at different frequencies. To reduce the measurement noise, the scope's 250MHz lowpass filter is turned on.
Figure 4. Voltage waveform across the 10Ω resistor at 26.99500MHz (-185PPM).

Figure 5. Voltage waveform across the 10Ω resistor at 27.00000MHz (0PPM).
In calculating the crystal's power level, we consider two scenarios: (1) the worst case (that is, the
maximum R value), and (2) the typical case.

The R value shown in the DSX530GA data sheet is 50Ω. This is, however, the absolute maximum for
the 12MHz to 54MHz frequency range. Typically, the higher the crystal frequency, the less the R value.
We consulted with KDS America and were informed that for a 27MHz DSX530GA crystal, the maximum
R value (case #1) is less than 40Ω, and the typical value (case #2) is 20Ω. As shown in the plots above,
the crystal currents have saw-tooth waveforms. We thus use Equation 4 to calculate the crystal RMS
current from its peak-to-peak value. Therefore, the crystal's calculated power levels are shown in Table
1.

Table 1. Measured Results and Crystal Power Levels

<table>
<thead>
<tr>
<th>Frequency MHz/PPM</th>
<th>Peak-to-Peak Voltage on 10Ω (mV)</th>
<th>Peak-to-Peak Current (mA)</th>
<th>RMS(1) Current (Ic)</th>
<th>Max Power (mΩ) Ic² x R R = 40Ω</th>
<th>Typ Power (mΩ) Ic² x R R = 20Ω</th>
</tr>
</thead>
<tbody>
<tr>
<td>26.995/-185</td>
<td>72</td>
<td>7.2</td>
<td>3.6/√3</td>
<td>172.8</td>
<td>86.4</td>
</tr>
<tr>
<td>27.000 0</td>
<td>88</td>
<td>8.8</td>
<td>4.4/√3</td>
<td>258.13</td>
<td>129</td>
</tr>
<tr>
<td>27.004/148</td>
<td>86</td>
<td>8.6</td>
<td>4.3/√3</td>
<td>246.5</td>
<td>123.2</td>
</tr>
</tbody>
</table>

Note: (1) The RMS value of a saw-tooth waveform is 1/2 √3 of the peak-to-peak value.

Conclusions

We can make some observations from Table 1.
1. In the worst case, the crystal power level of the MAX9485 is about 40µW less than the maximum power level (300µW) of DSX530GA.
2. In the typical case, with the realistic value of R at 27MHz, the power level is well under the maximum power level (300µW).
3. For the VCXO in the MAX9485, the highest crystal current occurs at the middle of the tuning range and the power variation is relatively small within the tuning range.

Reference
[1] MAX9485 data sheet
[2] MAX9485 application note 2097, "Using a VCXO (Voltage-Controlled Crystal Oscillator) as a Clock (CLK) Generator."
[4] DSX530GA data sheet

Related Parts

<table>
<thead>
<tr>
<th>Part</th>
<th>Description</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAX9485</td>
<td>Programmable Audio Clock Generator</td>
<td>Free</td>
</tr>
<tr>
<td>MAX9489</td>
<td>Multiple-Output Network Clock Generator</td>
<td></td>
</tr>
<tr>
<td>MAX9491</td>
<td>Factory-Programmable, Single PLL Clock Generator</td>
<td>Free</td>
</tr>
</tbody>
</table>

More Information
For Technical Support: http://www.maximintegrated.com/support
For Samples: http://www.maximintegrated.com/samples
Other Questions and Comments: http://www.maximintegrated.com/contact

Application Note 3736: http://www.maximintegrated.com/an3736
APPLICATION NOTE 3736, AN3736, AN 3736, APP3736, Appnote3736, Appnote 3736
Copyright © by Maxim Integrated Products
Additional Legal Notices: http://www.maximintegrated.com/legal