

PKG, CDDES

[S16M] [S16SM+12]

NDTES:

- REFERENCE PKG, DUTLINE: 21-0041
- 2. LAND PATTERN COMPLIES TO: IPC7351A.
- 3. TOLERANCE: +/- 0.02 MM.
- 4. ALL DIMENSIONS APPLY TO PHFREE (+) PKG. CODE ONLY.
- ALL DIMENSIONS IN MM.

-DRAWING NOT TO SCALE-

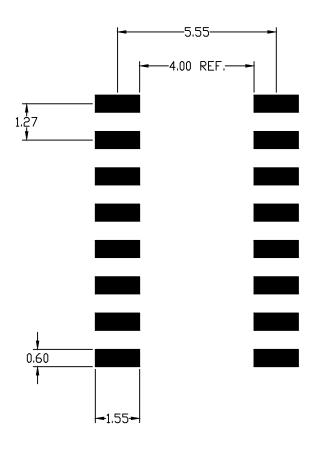
This document (including dimensions, notes & specs) is a recommendation based on typical circuit board manufacturing parameters. Since land pattern design depend on many factors unknown to Maxim (eg. user's board manufacturing specs), user must determine suitability for use. This document is subject to change without notice. Contact technical support at http://www.maxim-ic.com/support for further questions.

PACKAGE LAND PATTERN, 150 MILS SDIC (N)

APPROVAL DOCUMENT CONTROL NO. 90-0442 ROHINI KESAVAN

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OPTION FOR IMPROVED HV ISOLATION (CLEARANCE/CREEPAGE OF 4MM)



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TITLE: PACKAGE LAND PATTERN, 150 MILS SDIC (N)

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REV. 2